

FIG. 1A

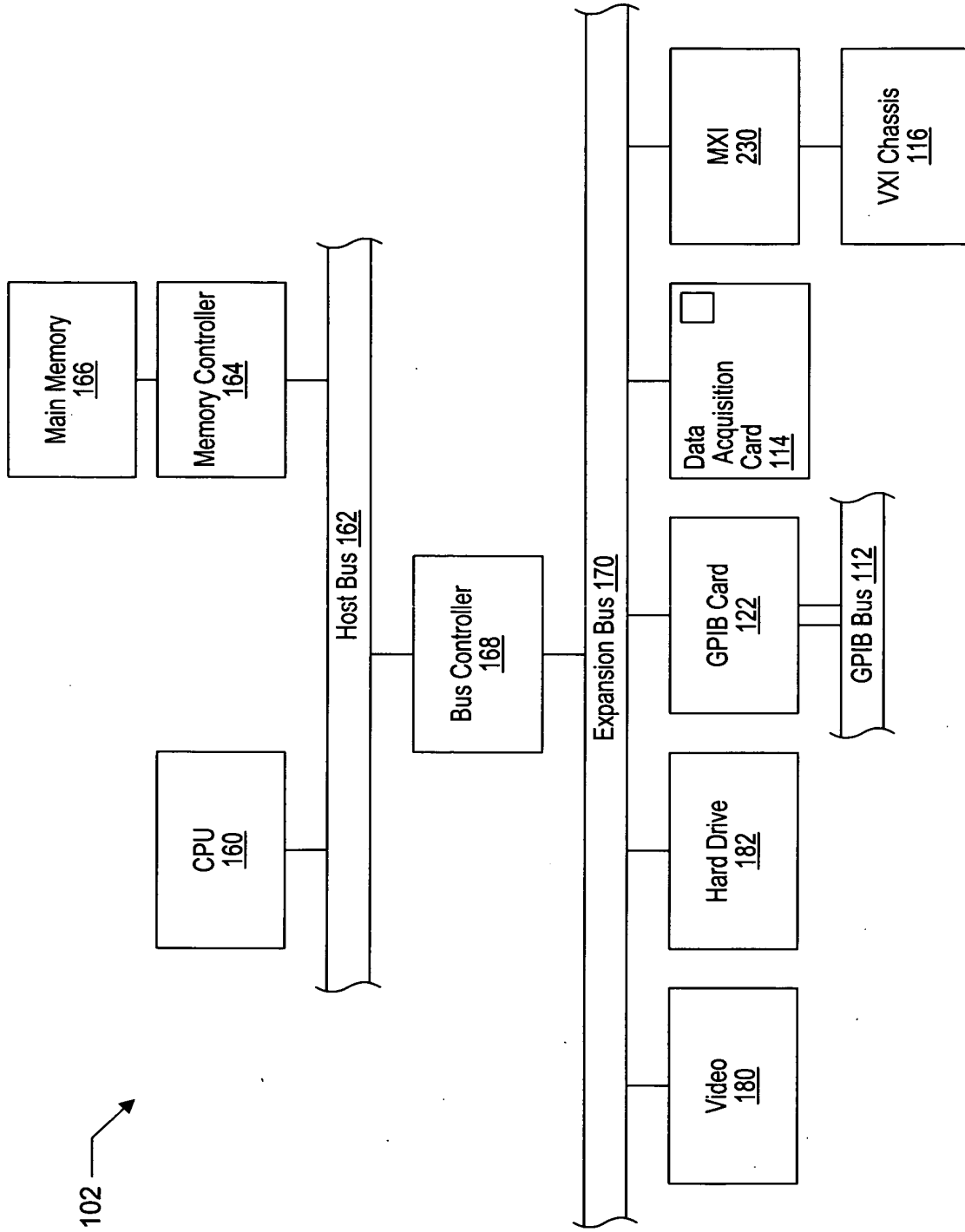


FIG. 2

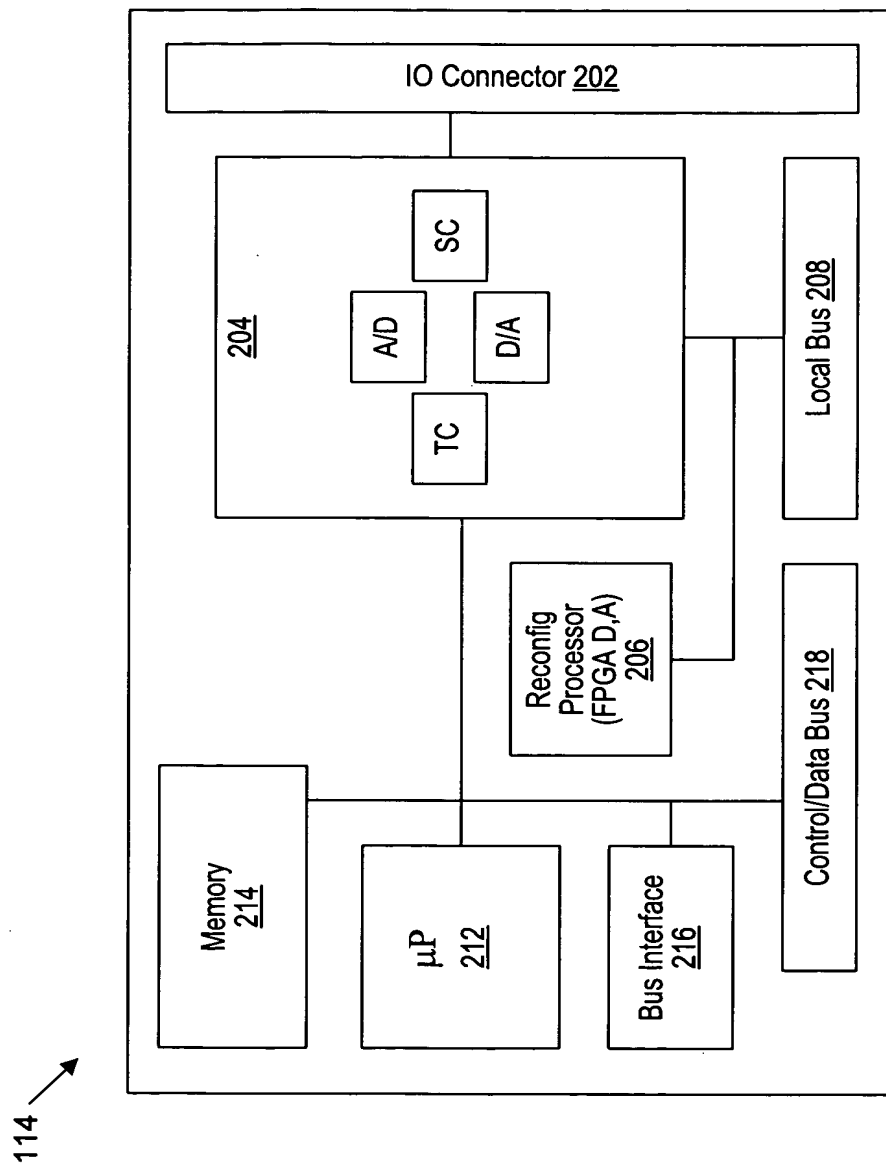


FIG. 3

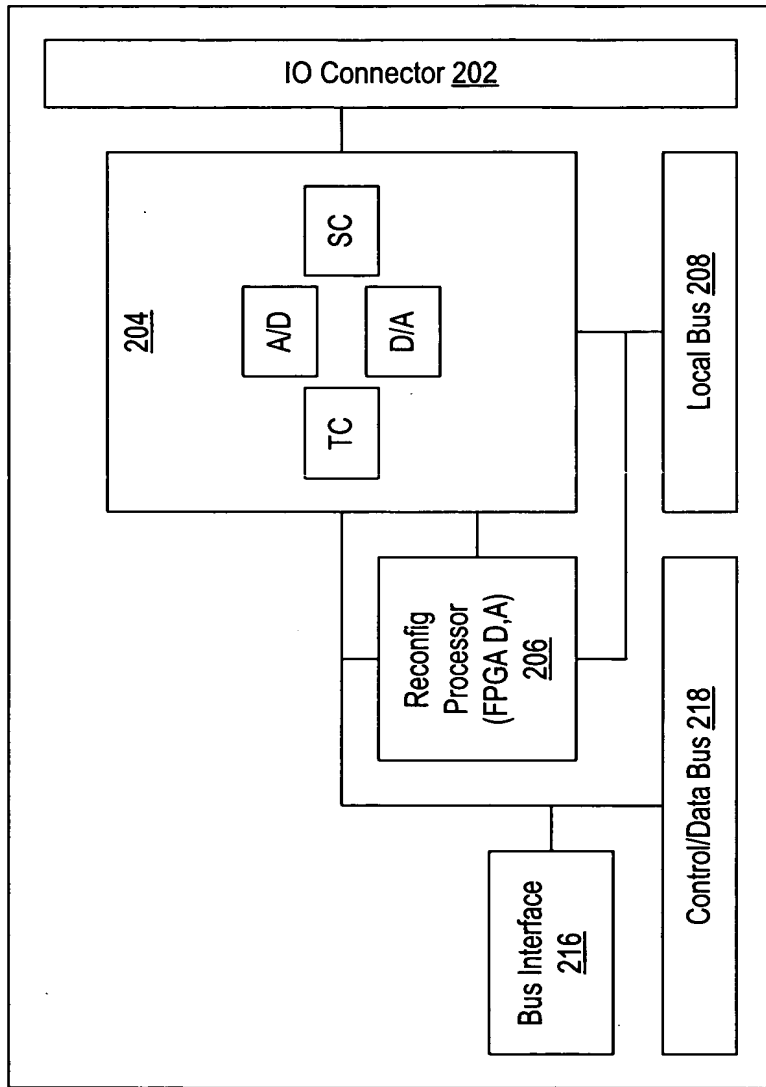


FIG. 3A

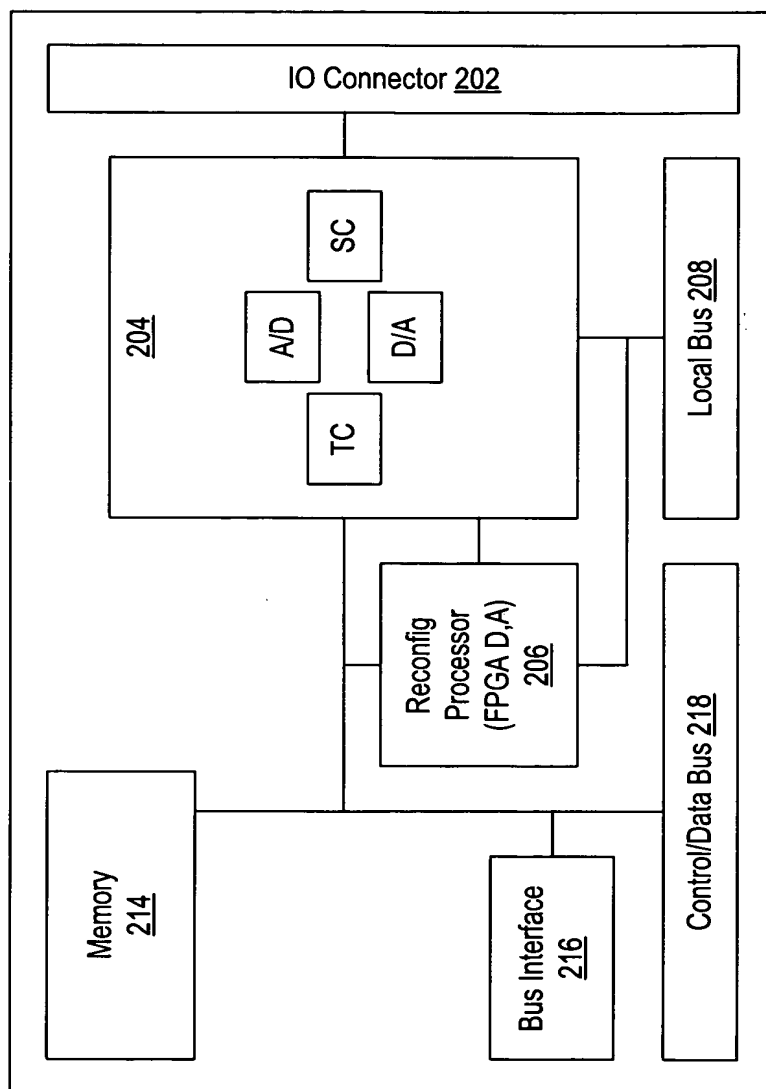


FIG. 3B

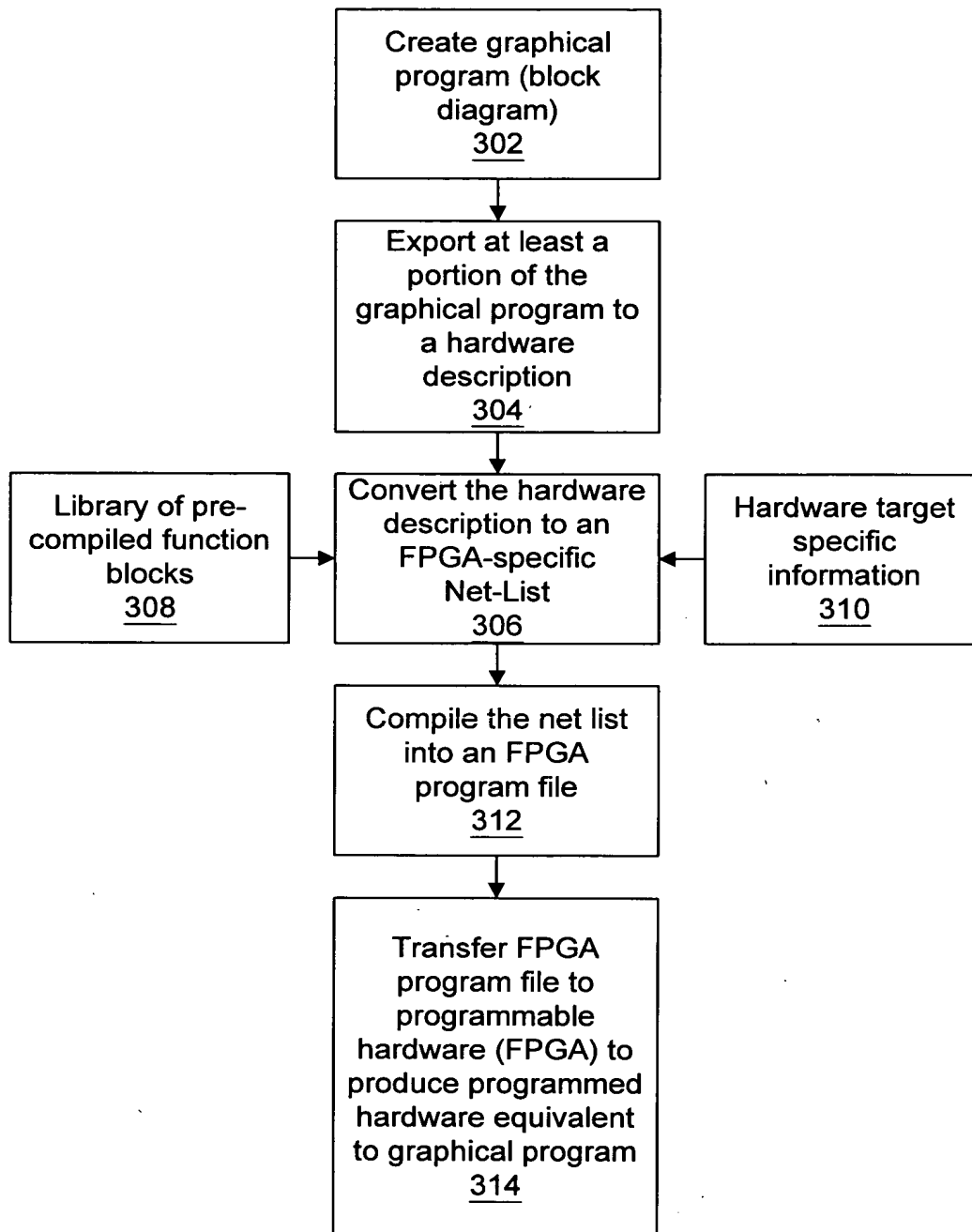


FIG. 4

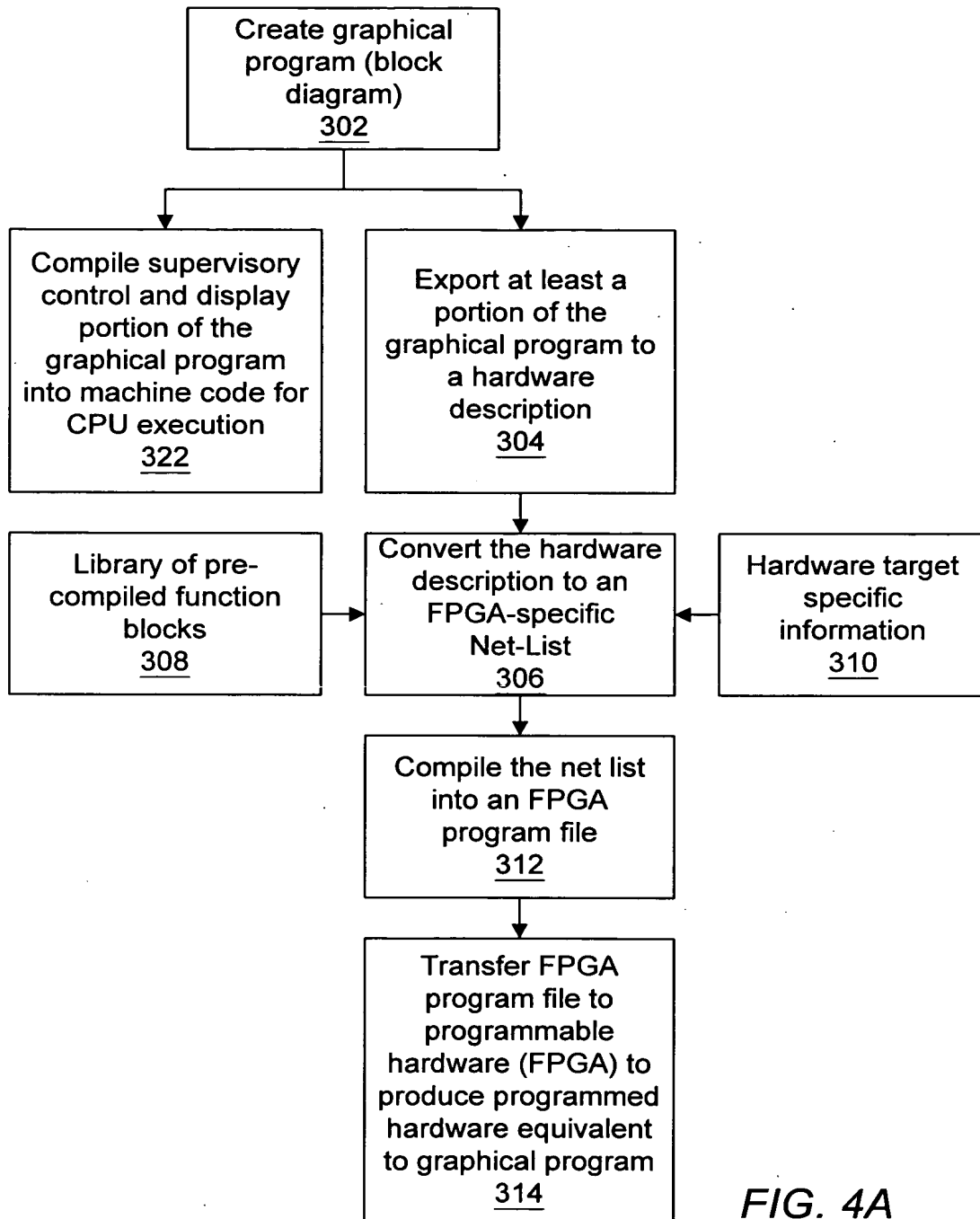


FIG. 4A

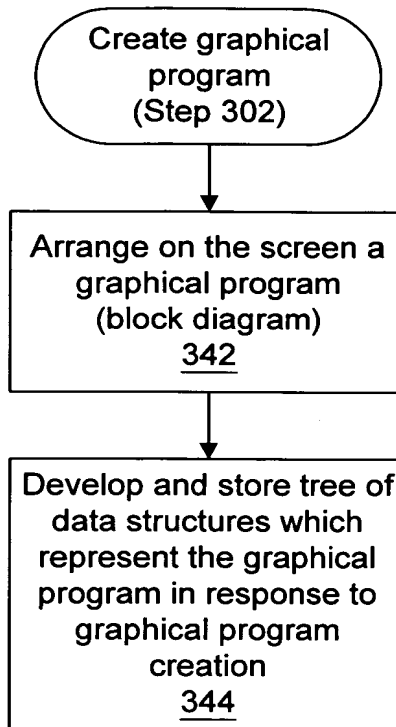


FIG. 5

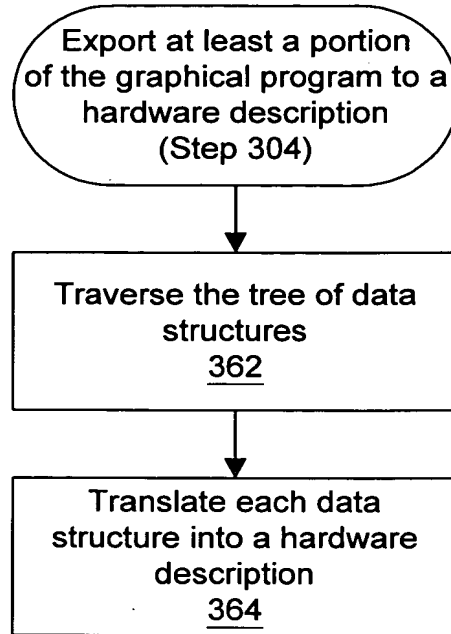


FIG. 6

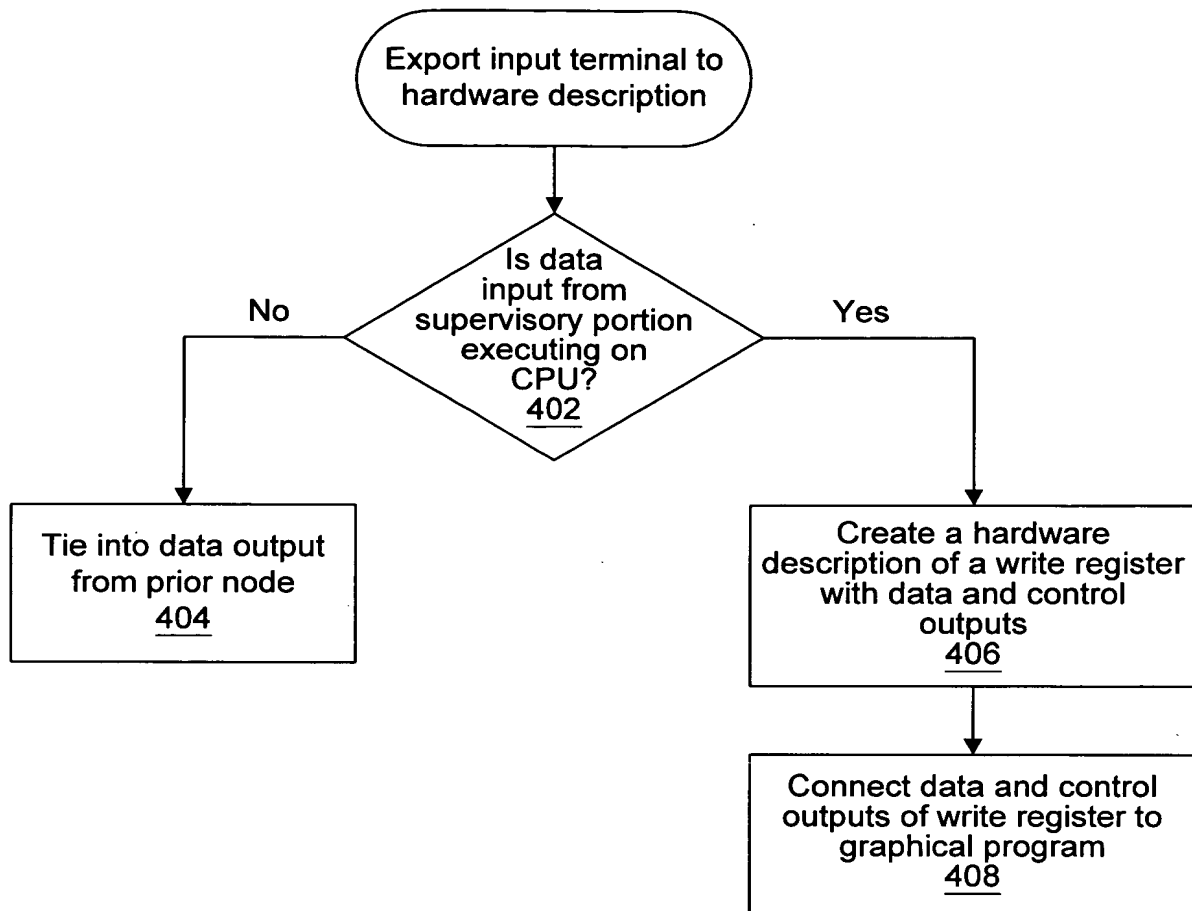


FIG. 7

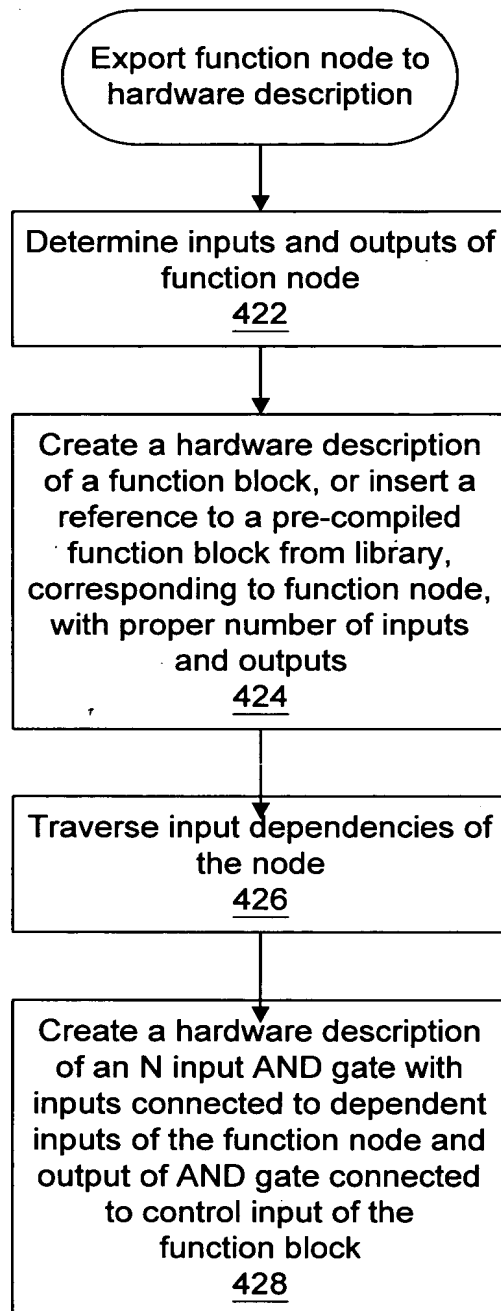


FIG. 8

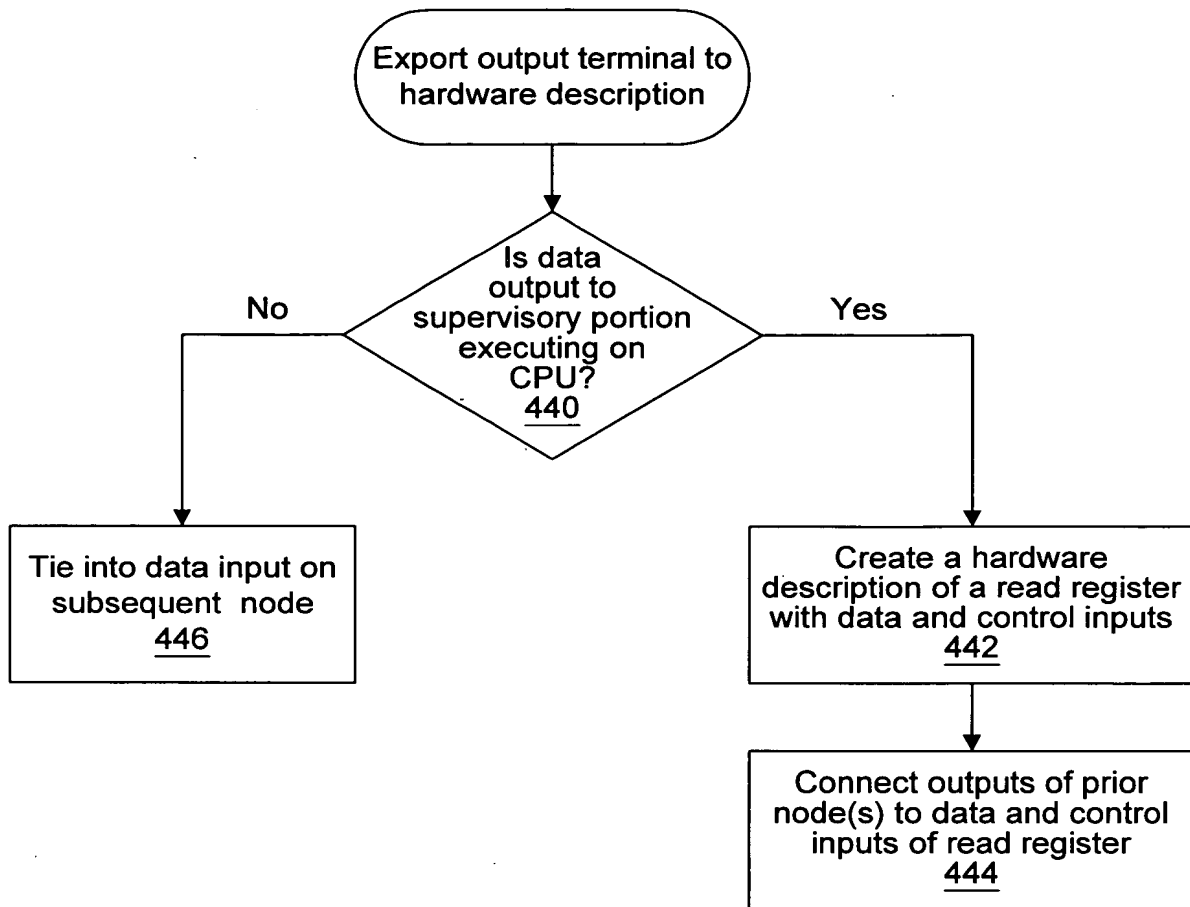
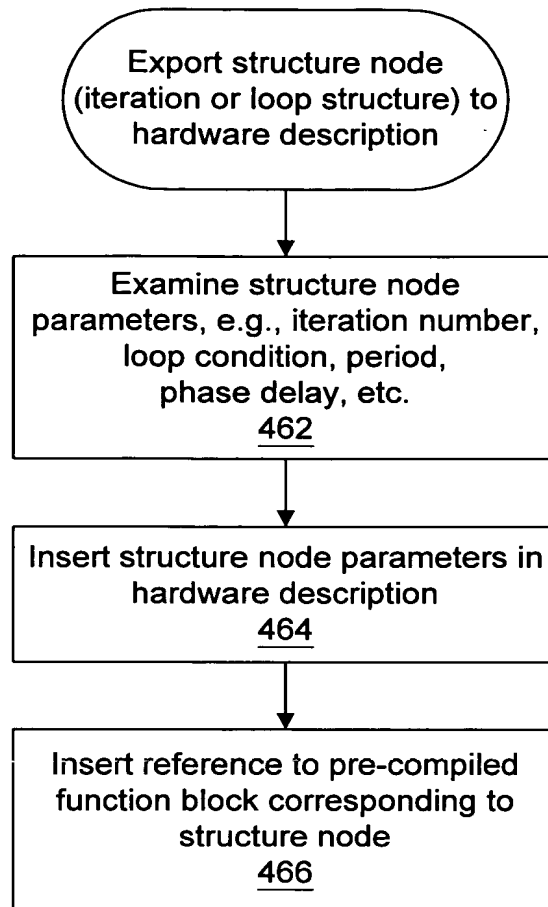


FIG. 9

*FIG. 10*

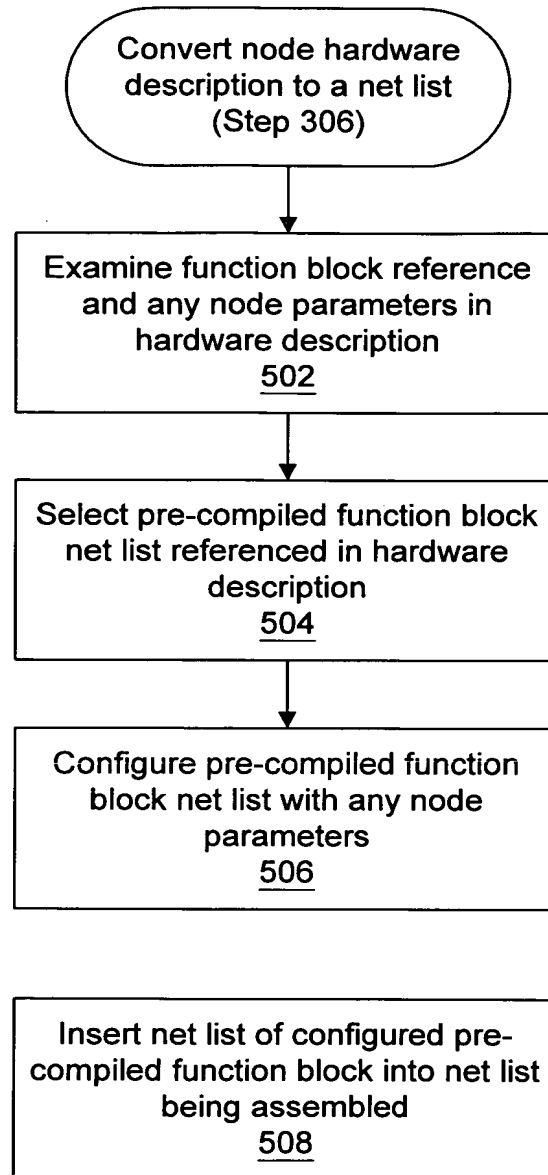


FIG. 11

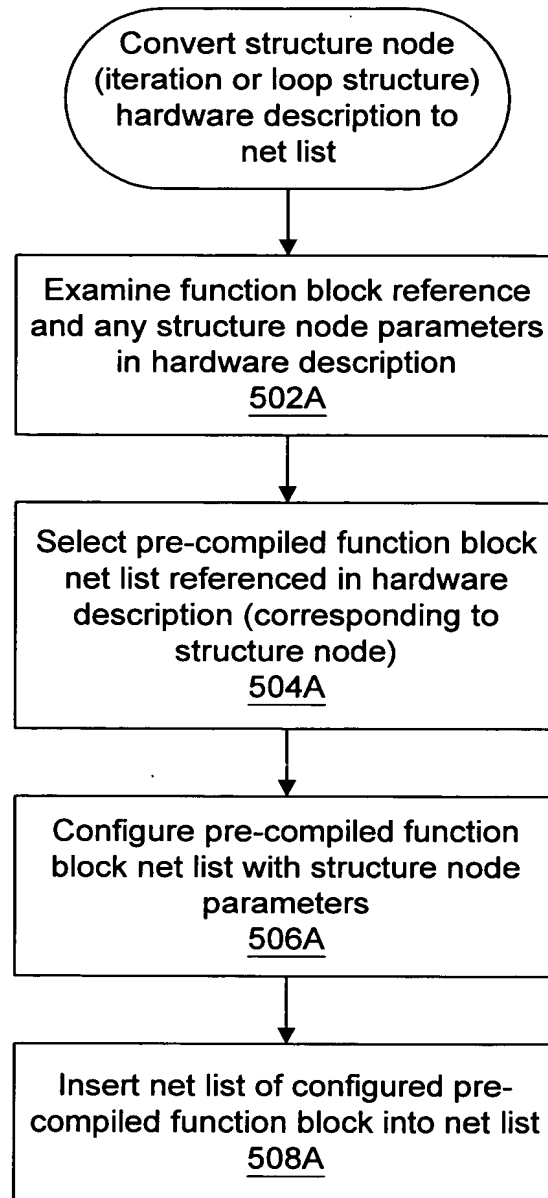


FIG. 12

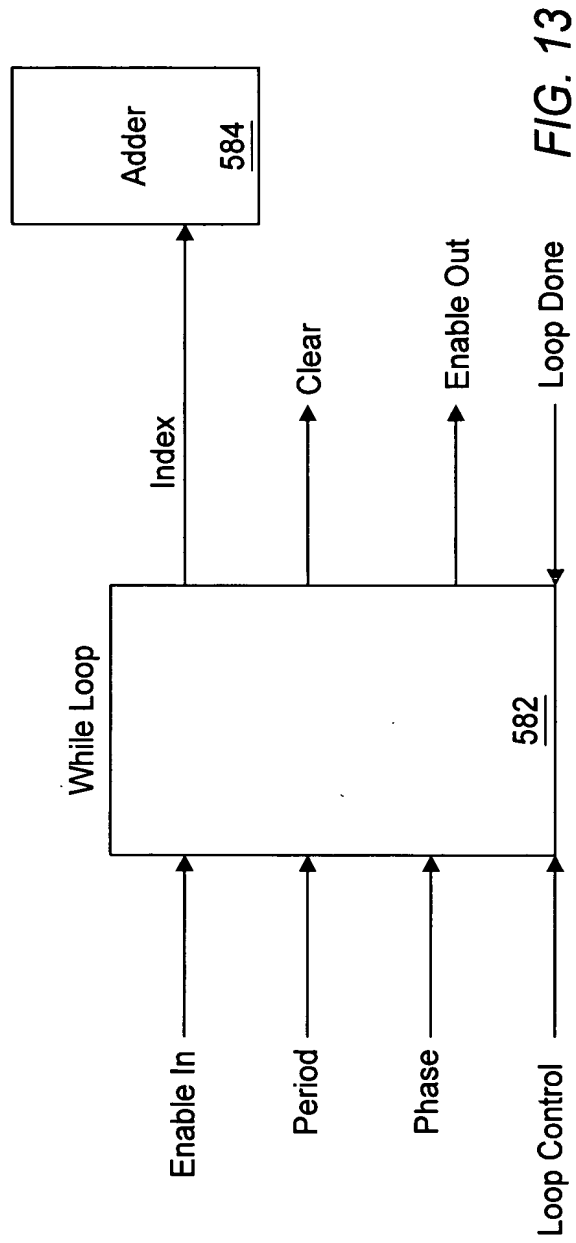


FIG. 13

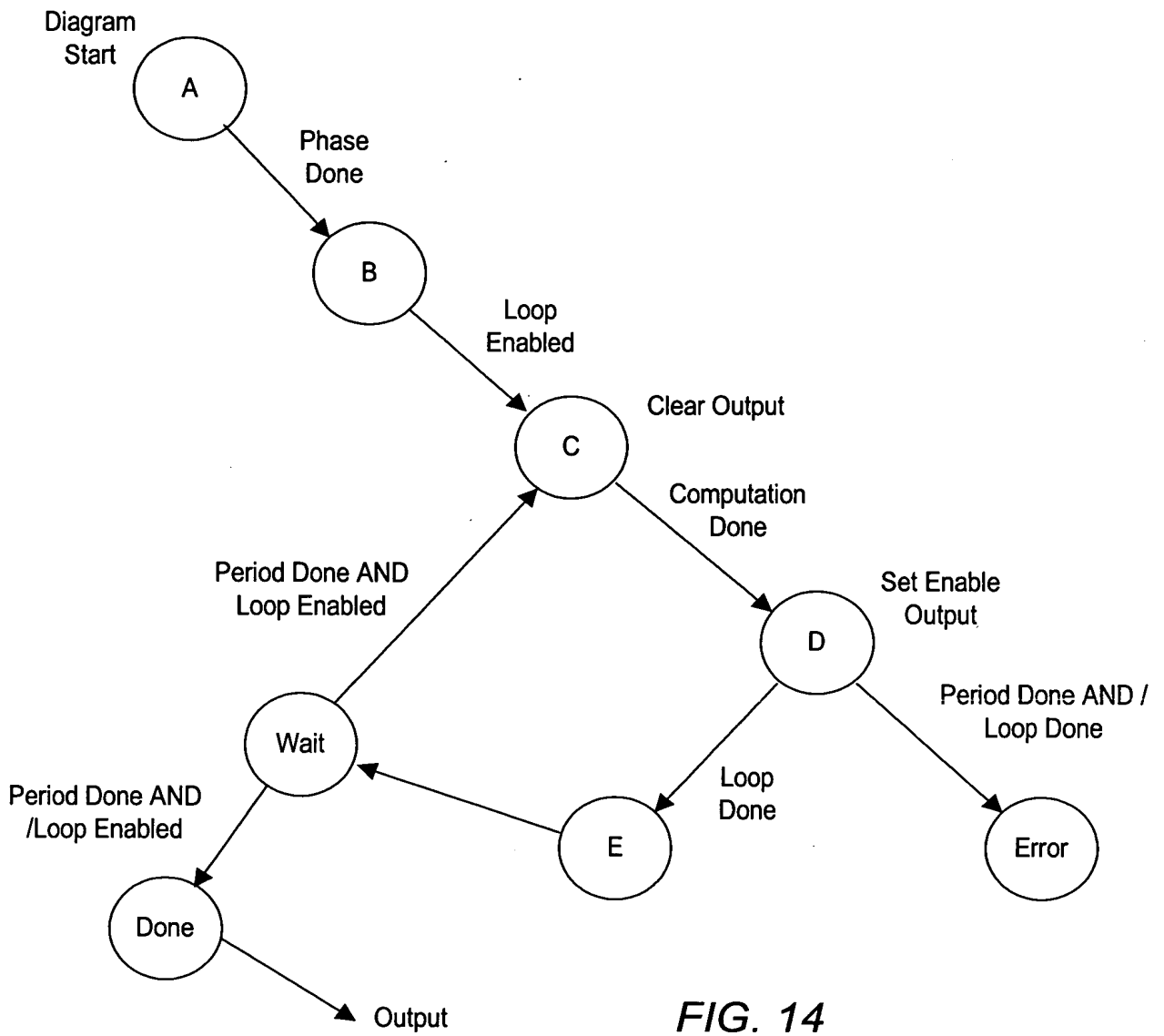
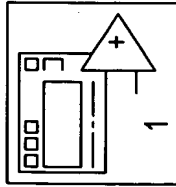


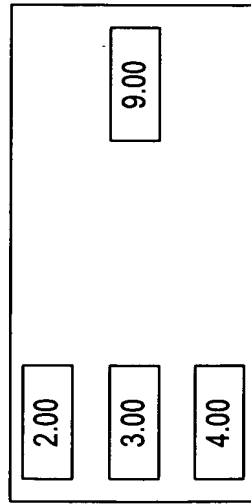
FIG. 14

Connector Pane



Untitled 1

Front Panel



Block Diagram

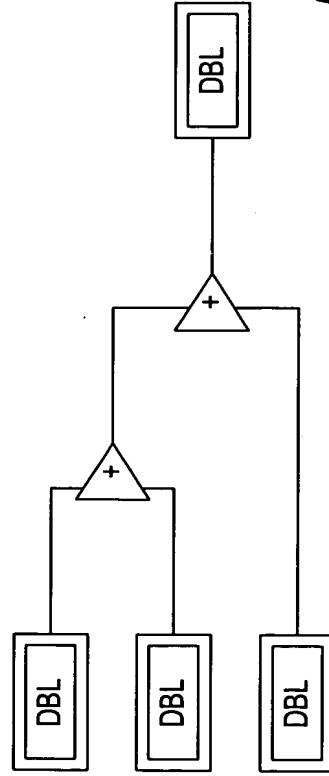


FIG. 15

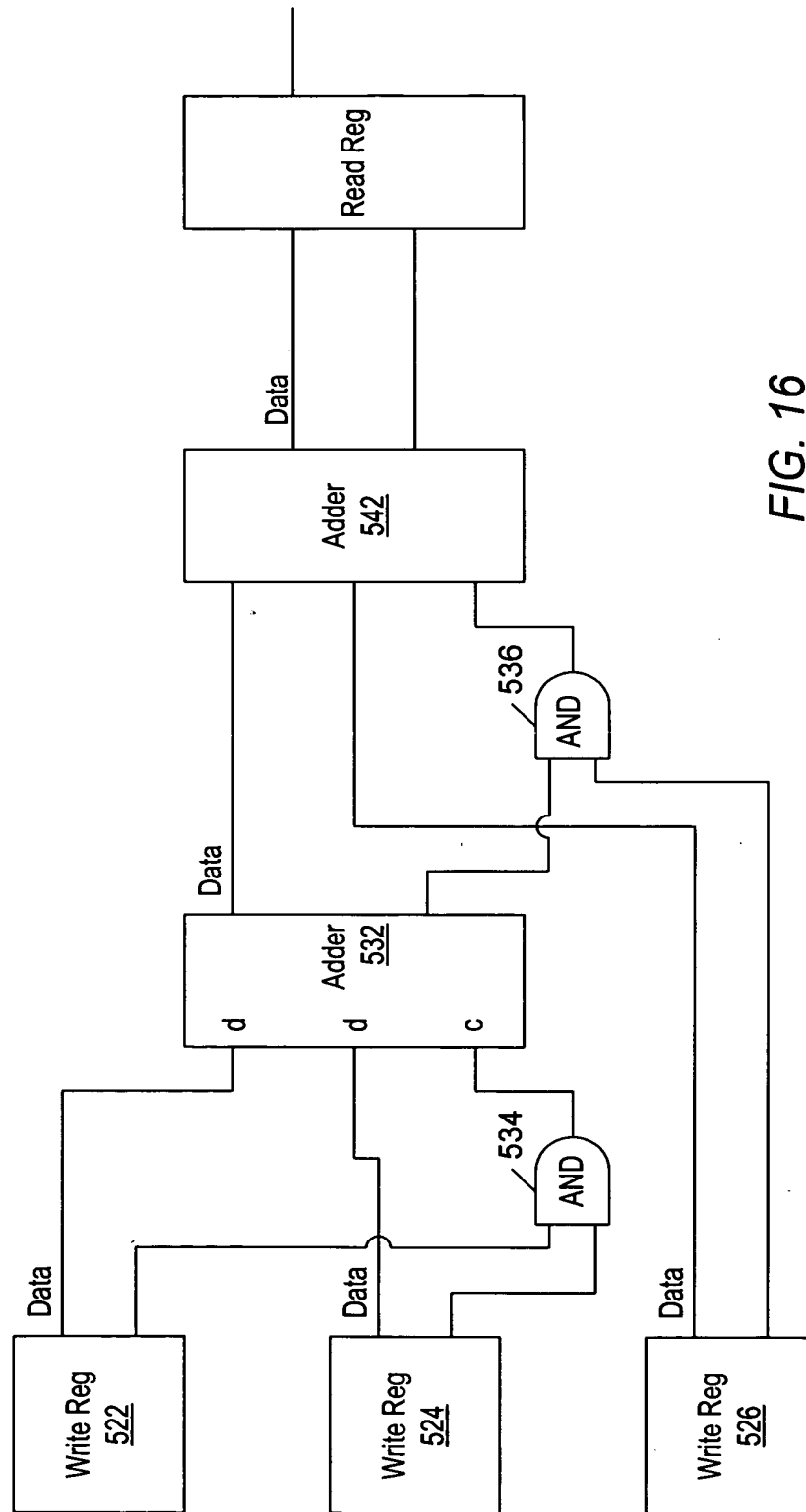


FIG. 16

4

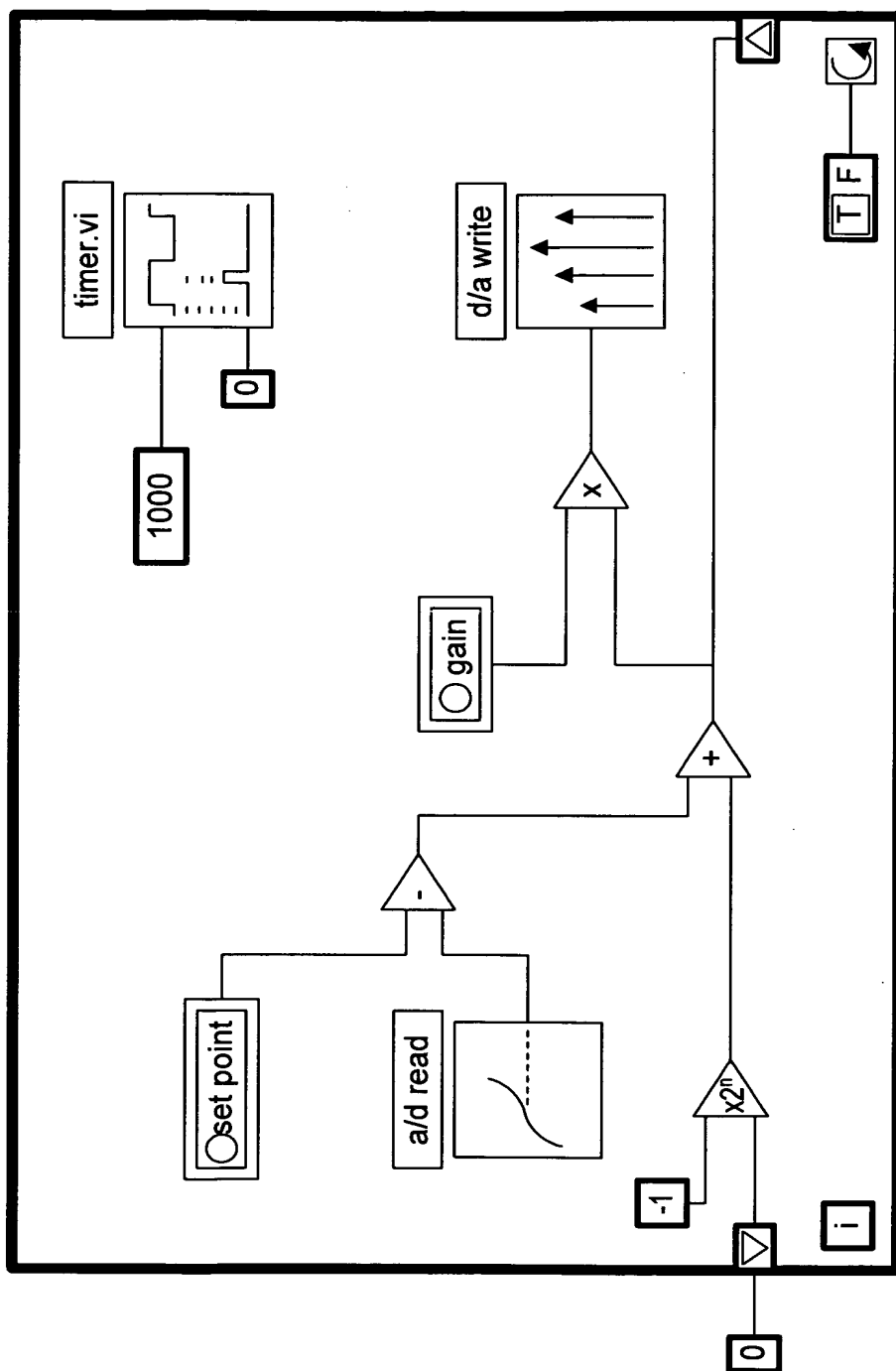


FIG. 17

LabView Data Structures

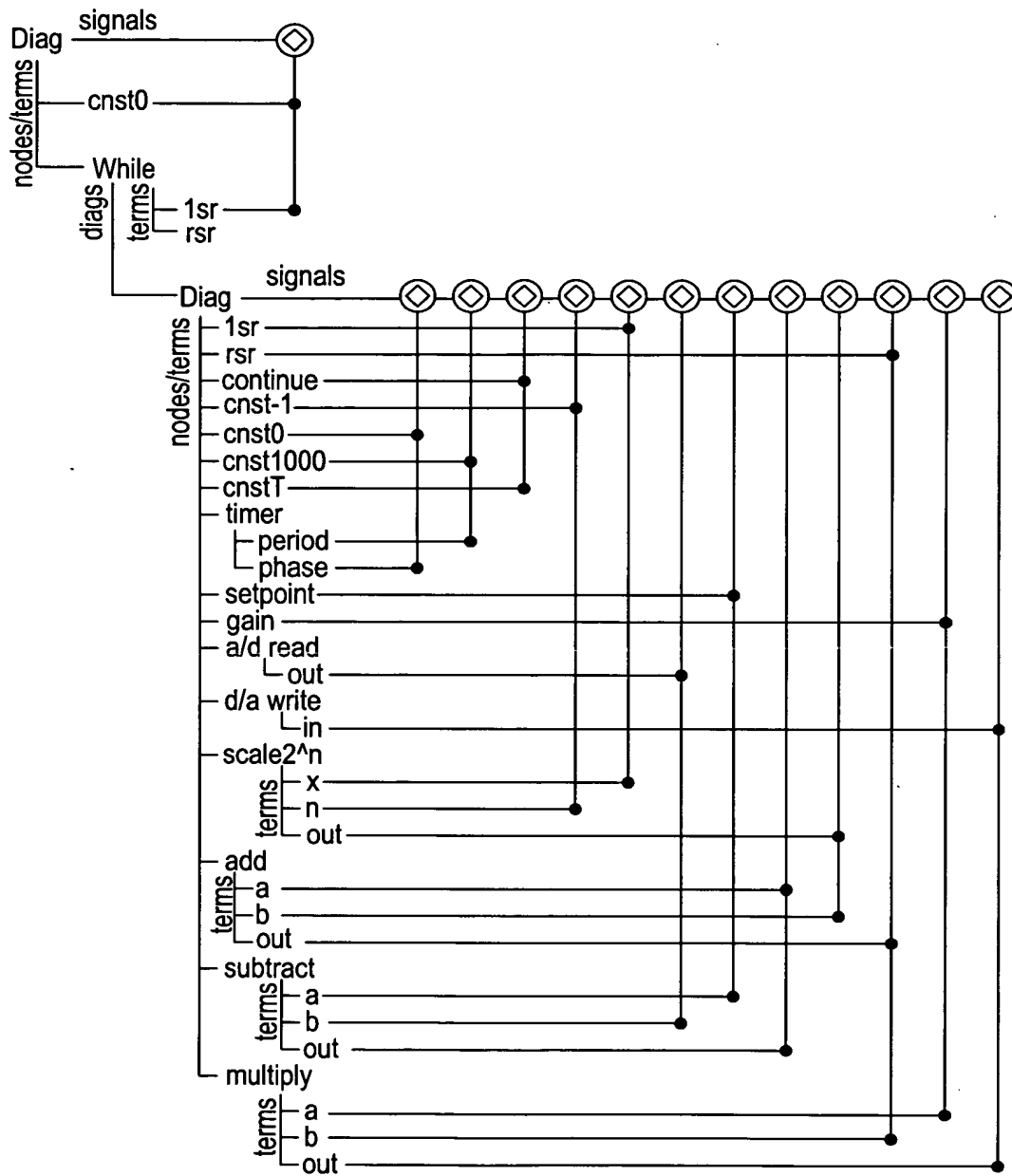


FIG. 18

